**ISA Simulator APEX**

CS 571 – Computer Architecture and Organization

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Assumptions:

APEX Simulator:

**Instruction Set:**

1. Register-to-register instructions: ADD, SUB, MOVC, MUL, AND, OR, EX-OR

ADD → ADD <dest> <src1> <src2>

SUB → SUB <dest> <src1> <src2>

MUL → MUL <dest> <src1> <src2>

AND → AND <dest> <src1> <src2>

MOVC → MOVC <dest> <src1> <src2>

OR → OR <dest> <src1> <src2>

EX-OR → EX-OR <dest> <src1> <src2>

1. BZ, BNZ, JUMP, BAL, HALT

BZ → BZ #literal (checks the z(zero) flag (slide 4 Page 213))

BNZ → BNZ #literal (goes to the instruction at #literal (Slide 2 Page 66))

JUMP → JUMP <src> @#literal (Page 2 of Project Description)

BAL →

HALT → Stops execution

1. LOAD, STORE

LOAD → LOAD <dest> <src1> <literal>

STORE → STORE <src1> <src2> <literal>

load:

store:

op:

opL: op <dest> <src1> <literal>